

Abstract of the Disclosure:

Differential power analysis on an integrated circuit is made more difficult by providing a circuit configuration for generating current pulses in the supply current of the integrated circuit. These additional pulses that are generated in the supply current are synchronous with the edges of the internal clock signal of the integrated circuit. In this case, the pulse shape and also the amplitude and the time profile are similar to the pulses in the supply current which are generated by other circuit sections, for example by processors or by some other digital logic, and in digital circuits, typically correspond to a charging curve of a capacitor via a resistor. The circuit generates these additional pulses by using a delay element.

MPW/kf